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IEM6300-12G

**Layer 2 Managed Industrial Ethernet Switch
Module**

Hardware Manual

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Preface

The manual has introduced the following contents:

- Product Overview
- Encapsulation and size
- Pin definition
- Interface Description

Audience

This manual applies to the following engineers:

- Network administrators
- Technical support engineers
- Hardware engineers

Conventions

Format	Description
" "	Words with "" represent the interface words. Such as: "Port No.".
>	Multi-level path is separated by ">". Such as opening the local connection path description: Open "Control Panel> Network Connection> Local Area Connection".
Light Blue Font	It represents the words clicked to achieve hyperlink. The font color is as follows: 'Light Blue'.
About this chapter	The section 'about this chapter' provide links to various sections of this chapter, as well as links to the Principles Operations Section of this chapter.

Symbols

Format	Description
 Notice	Remind the announcements in the operation, improper

Format	Description
	operation may result in data loss or equipment damage.
 Warning	Pay attention to the notes on the mark, improper operation may cause personal injury.
 Note	Make a necessary supplementary instruction for operation description.
 Key	Configuration, operation, or tips for device usage.
 Tips	Pay attention to the operation or information to ensure success device configuration or normal working.

Revision Record

Version No.	Date	Revision note
01	10/08/2022	Product release

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1 Product Overview

1.1 Product Introduction

This product is a layer 2 managed embedded industrial Ethernet switch module, which has the characteristics of high integration, small size, rich functions, simple and convenient operation. Including the following interface types:

- 8 Gigabit copper ports, support 10/100/1000Base-T(X) Ethernet.
- 4 Gigabit fiber ports (SFP slot), 1000Base-X.
- 1 Console port, used for command line to configure and debug this module.
- 1 alarm port, which can be extended to relay alarm.

1.2 Product Specification

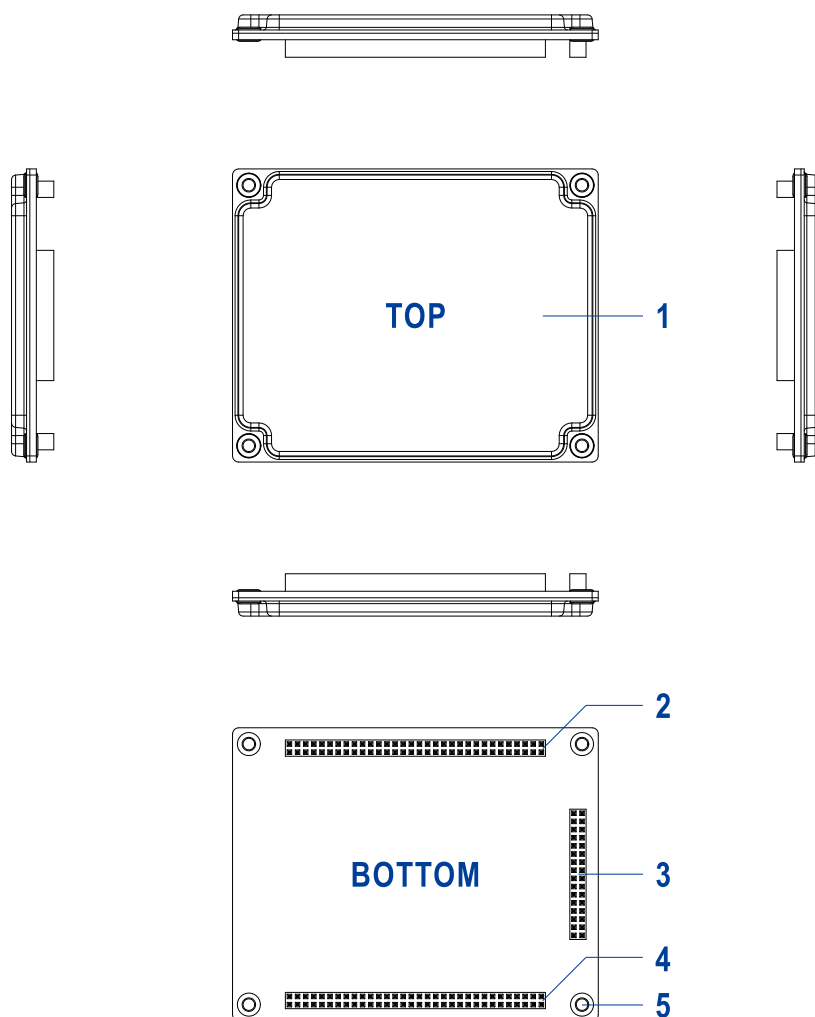
Interface	
Gigabit copper port	10/100/1000Base-T(X), Automatic Flow Control, Full/Half Duplex Mode, MDI/MDI-X Autotuning
Gigabit SFP	1000Base-X SFP (SGMII)
Console port	CLI command management port
Alarm interface	Alarm input/output
Switch Property	
Backplane bandwidth	24G
Packet buffer size	4Mbit
MAC Address Table	8K
Power supply	
Input power supply	3.3VDC
Power consumption	

No-load	2.7W (including DEMO board@12VDC)
Full load	8.4W (including DEMO board@12VDC)
Working Environment	
Working temperature	-40~75℃
Storage temperature	-40~85℃
Working humidity	5%~95%
Physical Characteristic	
Mounting	Embedded installation (pin and female header type)
Dimension (W x H x D)	90mm×10.4mm×72mm

2 Dimension

2.1 Capsulation Design

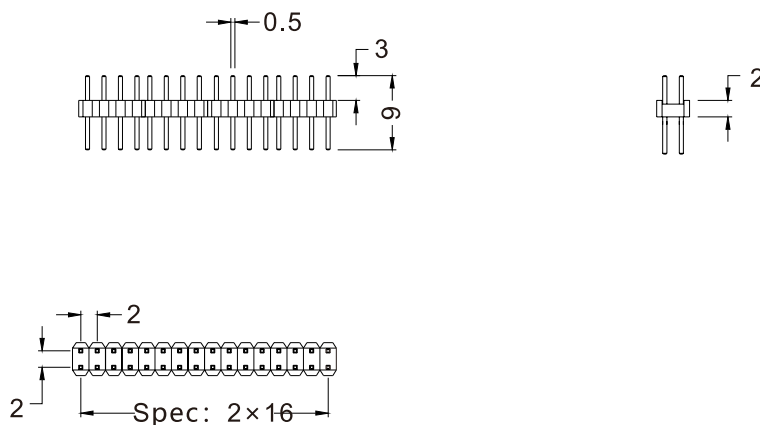
The diagram of module structure is as follows:



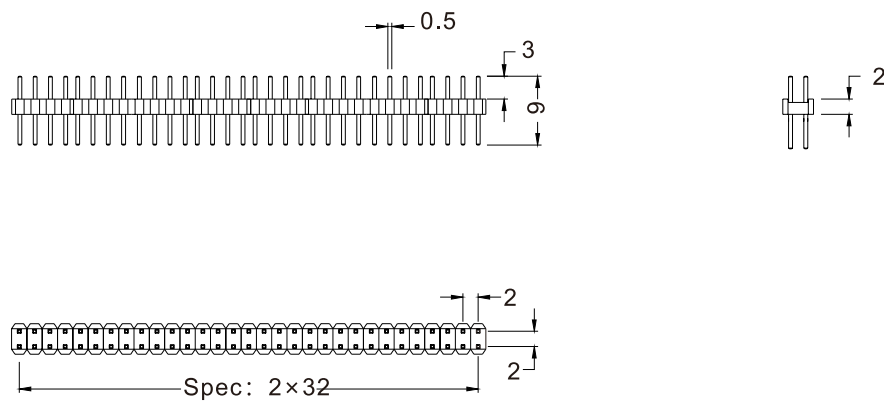
The details of each package label are shown in the following table:

Capsulation	Name	Specification	Note
1	A blade of thermal conductivity	90mm*72mm	External heat sink available
2	Connector B	Plug-in connector 2*32, pin spacing 2mm, connector height 4.3mm, square hole 0.5mm*0.5mm.	—
3	Connector C	Plug-in connector 2*16, pin spacing 2mm, connector height 4.3mm, square hole 0.5mm*0.5mm.	—
4	Connector A	Plug-in connector 2*32, pin spacing 2mm, connector height 4.3mm, square hole 0.5mm*0.5mm.	—
5	location hole	The diameter of the positioning hole is $3.1\pm0.15\text{mm}$, which is suitable for M3 screws. The distance from the center of the circle to the four sides of the PCB is 4mm.	The four positioning holes have the same specifications

The needle specifications corresponding to Connector C can refer to the following view, unit is mm:



The needle specifications corresponding to Connector A and B can refer to the following view, unit is mm:



2.2 Product Dimension

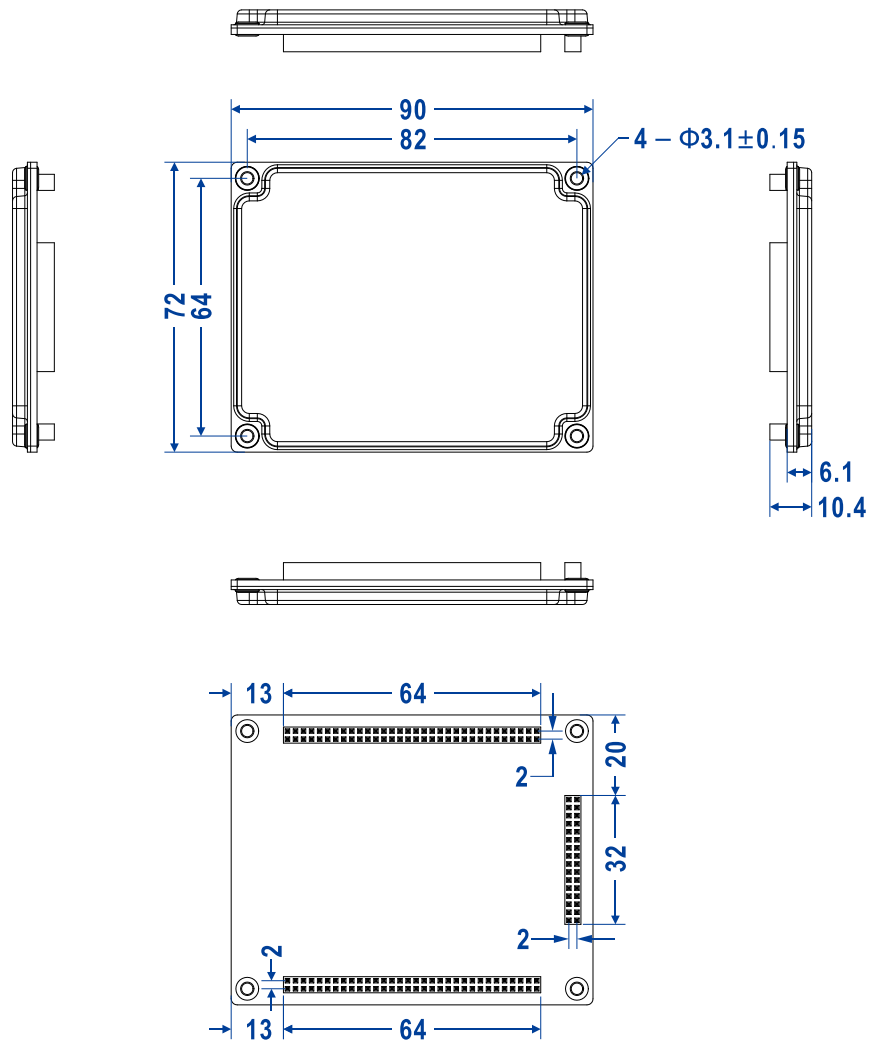
The diagram of module structure dimension is as follows:

Unit: mm



Note

The diameter of the positioning hole is 3.1 ± 0.15 mm, which is suitable for M3 screws.



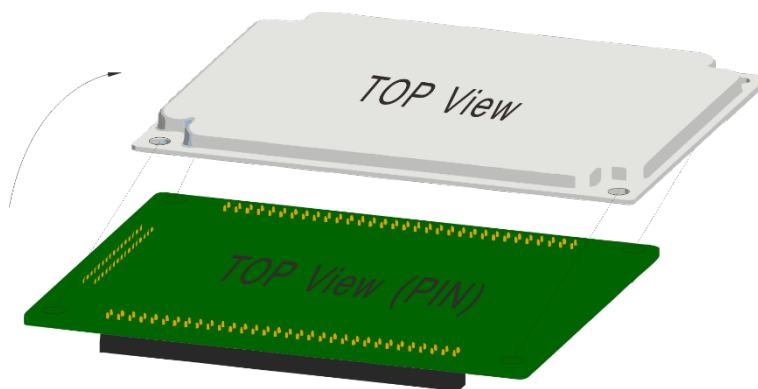
3 Pin Definition Description

3.1 Pin Definition View

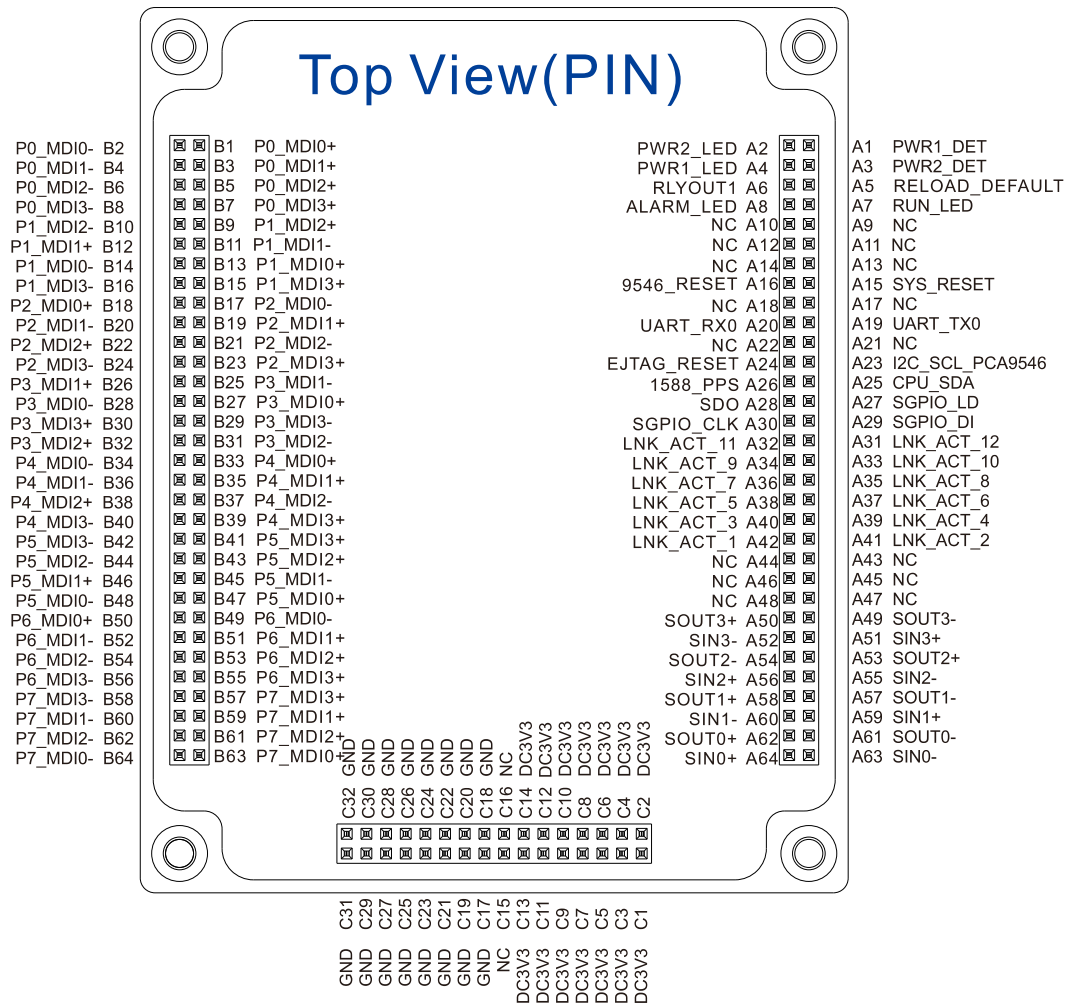


Note

In order to facilitate users to view pin definition when PCB board wiring, the pin definition diagram adopts the top view Angle as shown below.



Top view of module pin number and name is as follows:



3.2 Pin Definition Name Description

Pin identification	Note
MDI	Medium Dependent Interface
GND	Ground, signal ground
DET	Detection
NC	Not Connected
RESET	Reset
LED	Light Emitting Diode
DC3V3	3.3VDC input
SYS	System

3.3 Pin definition list

Pin Definition (A1-A64) of Connector A (J1)

Pin No.	PIN name	Pin No.	PIN name
A1	PWR1_DET	A2	PWR2_LED
A3	PWR2_DET	A4	PWR1_LED
A5	RELOAD_DEFAULT	A6	RLYOUT1
A7	RUN_LED	A8	ALARM_LED
A9	NC	A10	NC
A11	NC	A12	NC
A13	NC	A14	NC
A15	SYS_RESET	A16	9546_RESET
A17	NC	A18	NC
A19	UART_TX0	A20	UART_RX0
A21	NC	A22	NC
A23	I2C_SCL_PCA9546	A24	EJTAG_RESET
A25	CPU_SDA	A26	1588_PPS
A27	SGPIO_LD	A28	SDO
A29	SGPIO_DI	A30	SGPIO_CLK
A31	LNK_ACT_12	A32	LNK_ACT_11
A33	LNK_ACT_10	A34	LNK_ACT_9
A35	LNK_ACT_8	A36	LNK_ACT_7
A37	LNK_ACT_6	A38	LNK_ACT_5
A39	LNK_ACT_4	A40	LNK_ACT_3
A41	LNK_ACT_2	A42	LNK_ACT_1
A43	NC	A44	NC
A45	NC	A46	NC
A47	NC	A48	NC
A49	SOUT3-	A50	SOUT3+
A51	SIN3+	A52	SIN3-
A53	SOUT2+	A54	SOUT2-
A55	SIN2-	A56	SIN2+
A57	SOUT1-	A58	SOUT1+
A59	SIN1+	A60	SIN1-
A61	SOUT0-	A62	SOUT0+

Pin No.	PIN name	Pin No.	PIN name
A63	SIN0-	A64	SIN0+

Pin Definition (B1-B64) of Connector B (J2)

Pin No.	PIN name	Pin No.	PIN name
B1	P0_MDI0+	B2	P0_MDI0-
B3	P0_MDI1+	B4	P0_MDI1-
B5	P0_MDI2+	B6	P0_MDI2-
B7	P0_MDI3+	B8	P0_MDI3-
B9	P1_MDI2+	B10	P1_MDI2-
B11	P1_MDI1-	B12	P1_MDI1+
B13	P1_MDI0+	B14	P1_MDI0-
B15	P1_MDI3+	B16	P1_MDI3-
B17	P2_MDI0-	B18	P2_MDI0+
B19	P2_MDI1+	B20	P2_MDI1-
B21	P2_MDI2-	B22	P2_MDI2+
B23	P2_MDI3+	B24	P2_MDI3-
B25	P3_MDI1-	B26	P3_MDI1+
B27	P3_MDI0+	B28	P3_MDI0-
B29	P3_MDI3-	B30	P3_MDI3+
B31	P3_MDI2-	B32	P3_MDI2+
B33	P4_MDI0+	B34	P4_MDI0-
B35	P4_MDI1+	B36	P4_MDI1-
B37	P4_MDI2-	B38	P4_MDI2+
B39	P4_MDI3+	B40	P4_MDI3-
B41	P5_MDI3+	B42	P5_MDI3-
B43	P5_MDI2+	B44	P5_MDI2-
B45	P5_MDI1-	B46	P5_MDI1+
B47	P5_MDI0+	B48	P5_MDI0-
B49	P6_MDI0-	B50	P6_MDI0+
B51	P6_MDI1+	B52	P6_MDI1-
B53	P6_MDI2+	B54	P6_MDI2-
B55	P6_MDI3+	B56	P6_MDI3-
B57	P7_MDI3+	B58	P7_MDI3-
B59	P7_MDI1+	B60	P7_MDI1-

Pin No.	PIN name	Pin No.	PIN name
B61	P7_MDI2+	B62	P7_MDI2-
B63	P7_MDI0+	B64	P7_MDI0-

Pin Definition (C1-C32) of Connector C (J3)

Pin No.	PIN name	Pin No.	PIN name
C1	DC3V3	C2	DC3V3
C3	DC3V3	C4	DC3V3
C5	DC3V3	C6	DC3V3
C7	DC3V3	C8	DC3V3
C9	DC3V3	C10	DC3V3
C11	DC3V3	C12	DC3V3
C13	DC3V3	C14	DC3V3
C15	NC	C16	NC
C17	GND	C18	GND
C19	GND	C20	GND
C21	GND	C22	GND
C23	GND	C24	GND
C25	GND	C26	GND
C27	GND	C28	GND
C29	GND	C30	GND
C31	GND	C32	GND

3.4 Detailed Pin Definition Description

3.4.1 Gigabit Ethernet Port

Pin definition description of Gigabit Ethernet port:

PIN name	Pin No.	Type	Function description
P0_MDI0+	B1	Input / output	Positive end of 1000M Ethernet interface data receiving. When the port is configured as a 1000M copper port, P[0:7]_MDI0+ is directly connected to the
P1_MDI0+	B13		
P2_MDI0+	B18		
P3_MDI0+	B27		
P4_MDI0+	B33		

PIN name	Pin No.	Type	Function description
P5_MDI0+	B47		network transformer.
P6_MDI0+	B50		When this pin is not used, it recommended be left suspended.
P7_MDI0+	B63		
P0_MDI0-	B2	Input / output	Negative end of 1000M Ethernet interface data receiving.
P1_MDI0-	B14		- When the port is configured as a 1000M copper port, P[0:7]_MDI0- is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P2_MDI0-	B17		
P3_MDI0-	B28		
P4_MDI0-	B34		
P5_MDI0-	B48		
P6_MDI0-	B49		
P7_MDI0-	B64		
P0_MDI1+	B3	Input / output	Positive end of 1000M Ethernet interface data transmission.
P1_MDI1+	B12		- When the port is configured as a 1000M copper port, P[0:7]_MDI1+ is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P2_MDI1+	B19		
P3_MDI1+	B26		
P4_MDI1+	B35		
P5_MDI1+	B46		
P6_MDI1+	B51		
P7_MDI1+	B59		
P0_MDI1-	B4	Input / output	Negative end of 1000M Ethernet interface data transmission.
P1_MDI1-	B11		- When the port is configured as a 1000M copper port, P[0:7]_MDI1- is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P2_MDI1-	B20		
P3_MDI1-	B25		
P4_MDI1-	B36		
P5_MDI1-	B45		
P6_MDI1-	B52		
P7_MDI1-	B60		
P0_MDI2+	B5	Input / output	Positive end of 1000M Ethernet interface data receiving.
P1_MDI2+	B9		- When the port is configured as a 1000M copper port, P[0:7]_MDI2+ is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P2_MDI2+	B22		
P3_MDI2+	B32		
P4_MDI2+	B38		
P5_MDI2+	B43		
P6_MDI2+	B53		
P7_MDI2+	B61		

PIN name	Pin No.	Type	Function description
P0_MDI2-	B6	Input / output	Negative end of 1000M Ethernet interface data receiving. - When the port is configured as a 1000M copper port, P[0:7]_MDI2- is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P1_MDI2-	B10		
P2_MDI2-	B21		
P3_MDI2-	B31		
P4_MDI2-	B37		
P5_MDI2-	B44		
P6_MDI2-	B54		
P7_MDI2-	B62		
P0_MDI3+	B7	Input / output	Positive end of 1000M Ethernet interface data transmission. - When the port is configured as a 1000M copper port, P[0:7]_MDI3+ is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P1_MDI3+	B15		
P2_MDI3+	B23		
P3_MDI3+	B30		
P4_MDI3+	B39		
P5_MDI3+	B41		
P6_MDI3+	B55		
P7_MDI3+	B57		
P0_MDI3-	B8	Input / output	Negative end of 1000M Ethernet interface data transmission. - When the port is configured as a 1000M copper port, P[0:7]_MDI3- is directly connected to the network transformer. - When this pin is not used, it recommended be left suspended.
P1_MDI3-	B16		
P2_MDI3-	B24		
P3_MDI3-	B29		
P4_MDI3-	B40		
P5_MDI3-	B42		
P6_MDI3-	B56		
P7_MDI3-	B58		

3.4.2 Gigabit Ethernet Port (SGMII)

Pin definition description of Gigabit Ethernet port (SGMII):

PIN name	Pin No.	Type	Function description
SIN0+	A64	Output	G0 sgmmii differential output signal
SIN0-	A63		When this pin is not used, it recommended be left suspended. Note: IN is the input of the opposite connected device, which corresponds to the output of the

PIN name	Pin No.	Type	Function description
			module.
SOUT0+	A62	Input	G0 sgmiidifferential input signal
SOUT0-	A61		When this pin is not used, it recommended be left suspended. Note: OUT is the output of the opposite connected device, which corresponds to the input of the module.
SIN1-	A60	Output	G1 sgmiidifferential output signal
SIN1+	A59		When this pin is not used, it recommended be left suspended.
SOUT1+	A58	Input	G1 sgmiidifferential input signal
SOUT1-	A57		When this pin is not used, it recommended be left suspended.
SIN2+	A56	Output	G2 sgmiidifferential output signal
SIN2-	A55		When this pin is not used, it recommended be left suspended.
SOUT2-	A54	Input	G2 sgmiidifferential input signal
SOUT2+	A53		When this pin is not used, it recommended be left suspended.
SIN3-	A52	Output	G3 sgmiidifferential output signal
SIN3+	A51		When this pin is not used, it recommended be left suspended.
SOUT3+	A50	Input	G3 sgmiidifferential input signal
SOUT3-	A49		When this pin is not used, it recommended be left suspended.

3.4.3 I²C

PIN name	Pin No.	Type	Function description
I2C_SCL_PCA9546	A23	Bidirectional	I ² C bus serial clock line, which needs to be pulled up
CPU_SDA	A25	Bidirectional	CPU serial data line, which needs to be pulled up

3.4.4 1588 Clock

PIN name	Pin No.	Type	Function description
1588_PPS	A26	Bidirectional	1588_PPS signal data output

3.4.5 SGPIO

PIN name	Pin No.	Type	Function description
SGPIO_LD	A27	Output	The last clock of a bit stream transmission
SDO	A28	Output	Serial data output bit stream
SGPIO_DI	A29	Input	Serial data input bit stream
SGPIO_CLK	A30	Output	Clock signal

3.4.6 Power and Ground Signal

Pin definition description of power supply and ground signal:

PIN name	Pin No.	Type	Function description
DC3V3	C1-C14	Input	3.3V voltage input
GND	C17-C32	Ground	Ground signal

3.4.7 Debugging Port

Pin definition description of CONSOLE port:

PIN name	Pin No.	Type	Function description
UART_RX0	A20	Input	Serial port 0 receiving Serial port 0 is only used for CONSOLE port
UART_TX0	A19	Output	Serial port 0 transmitting Serial port 0 is only used for CONSOLE port

3.4.8 Alarm interface

Pin definition description of alarm port:

PIN name	Pin No.	Type	Function description
PWR2_DET	A3	Input	Power 2 sampling test, the valid flag of the second power supply, active high
PWR1_DET	A1	Input	Power 1 sampling test, the valid flag of the first power supply, active high
RLYOUT1	A6	Output	Relay alarm output 1, active-high

3.4.9 Indicator

Pin definition description of indicator:

PIN name	Pin No.	Type	Function description
LNK_ACT_1	A42	Output	1000M Ethernet port Link/ACT indication Pin LNK_ACT_[0:7]. - When this pin outputs high level, it indicates that the corresponding port has no connection. - When this pin outputs low level, it indicates that the corresponding port has established a connection. - When this pin outputs alternating high and low levels, it indicates that the corresponding port has data transmission.
LNK_ACT_2	A41		
LNK_ACT_3	A40		
LNK_ACT_4	A39		
LNK_ACT_5	A38		
LNK_ACT_6	A37		
LNK_ACT_7	A36		
LNK_ACT_8	A35		
LNK_ACT_9	A34		
LNK_ACT_10	A33		
LNK_ACT_11	A32		
LNK_ACT_12	A31		
ALARM_LED	A8	Output	Alarm indicator output, active low, used to indicate power failure, relay action, disconnection of network port, etc.
PWR1_LED	A4	Output	Power indicator 1, active-low means the first power supply is valid
PWR2_LED	A2	Output	Power indicator 2, active-low means the second power supply is valid
RUN_LED	A7	Output	System running indicator, blinking 1s/time

PIN name	Pin No.	Type	Function description
			indicates that the CPU is running normally

3.4.10 Reset Interface

Pin definition description of reset interface:

PIN name	Pin No.	Type	Function description
SYS_RESET	A15	Output	Output pin of system reset signal. When the system is restarted, this pin outputs low-level signals to detect the system reset state. When not in use, it is recommended to be suspended.
9546_RESET	A16	Output	The reset pin provided for the external chip of the system, it is needed when the extended external chip is powered on or reset.
EJTAG_RESET	A24	Input	Input pin of manual reset, active low. When this pin is low level, the system will enter the reset and restart state.

3.4.11 Restore Factory Settings

PIN name	Pin No.	Type	Function description
RELOAD_DEFAULT	A5	Input	Restore factory settings. Active low for 2s, it is recommended to pull high.

3.4.12 Reserved Pins

PIN name	Pin No.	Type	Function description
NC	A9 A10 A11 A12 A13 A14 A17 A18 A21 A22 A43 A44	Reserved	Please suspend it

PIN name	Pin No.	Type	Function description
	A45 A46 A47 A48 C15 C16		

4 Reference circuit



Note

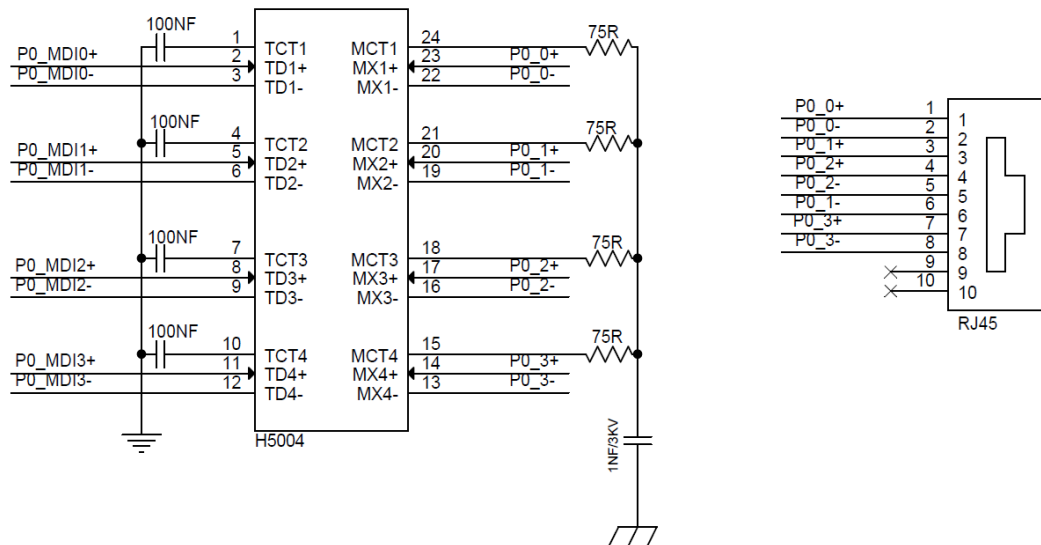
The ground signal in the reference circuit is identified as follows:

-  : GND, ground signal;
-  : FG, frame grounding.

4.1 Gigabit Ethernet Port

PIN name	Type	Configuration Method
P_MDI0/2+	Positive input/output end of data receiving	• When the port is configured to 1000M copper port, the pin is directly connected to the sending network transformer. • When this pin is not used, it recommended be left suspended.
P_MDI0/2-	Negative input/output end of data receiving	
P_MDI1/3+	Positive input/output end of data transmission	
P_MDI1/3-	Negative input/output end of data transmission	

Reference Circuit of Gigabit Ethernet Port



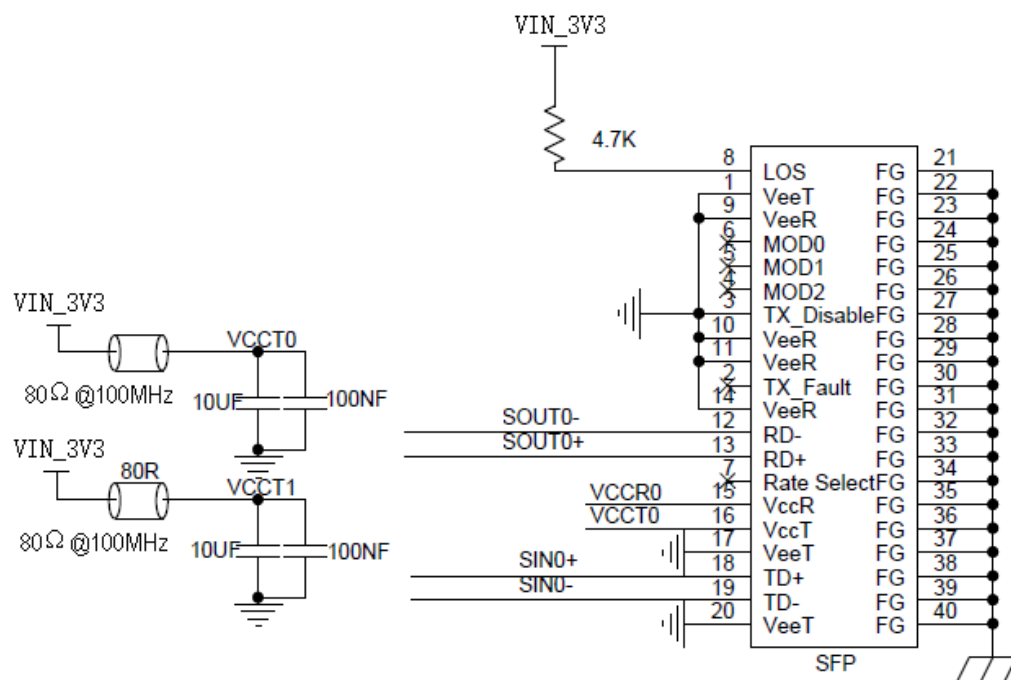
4.2 Gigabit Ethernet Port (SGMII)

The module provides four SGMII interfaces, which can support 1000base-X configuration, sfp optical module interfaces:

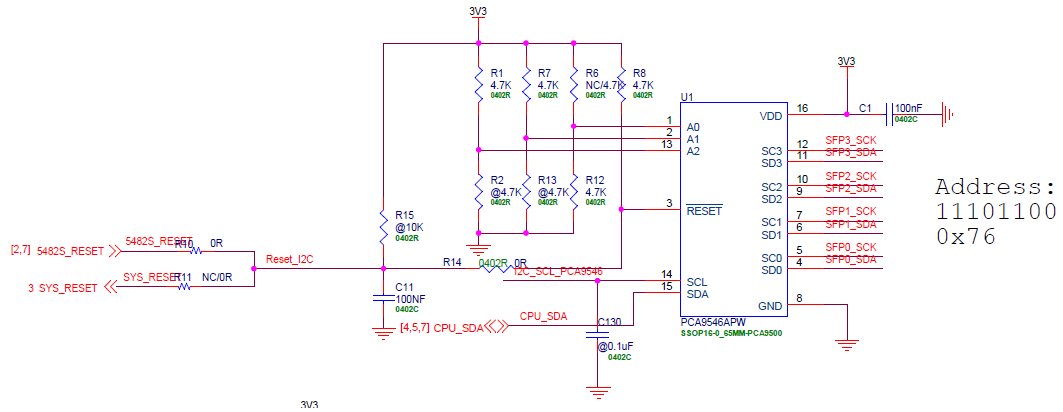
PIN name	Type	Configuration Method
SIN0+	Output	G0 sgmii differential output signal Note: IN is the input of the opposite connected device, which corresponds to the output of the module.
SIN0-		
SOUT0+	Input	G0 sgmii differential input signal Note: OUT is the output of the opposite connected device, which corresponds to the input of the module.
SOUT0-		
SIN1-	Output	G1 sgmii differential output signal
SIN1+		
SOUT1+	Input	G1 sgmii differential input signal
SOUT1-		
SIN2+	Output	G2 sgmii differential output signal
SIN2-		
SOUT2-	Input	G2 sgmii differential input signal
SOUT2+		
SIN3-	Output	G3 sgmii differential output signal
SIN3+		

SOUT3+	Input	G3 sgmiidifferential input signal
SOUT3-		
I2C_SCL_PCA9546	Bidirectional	I ² C bus serial clock line, which needs to be pulled up
CPU_SDA	Bidirectional	CPU serial data line, which needs to be pulled up
SGPIO_LD	Output	The last clock of a bit stream transmission
SDO	Output	Serial data output bit stream
SGPIO_DI	Input	Serial data input bit stream
SGPIO_CLK	Output	Clock signal

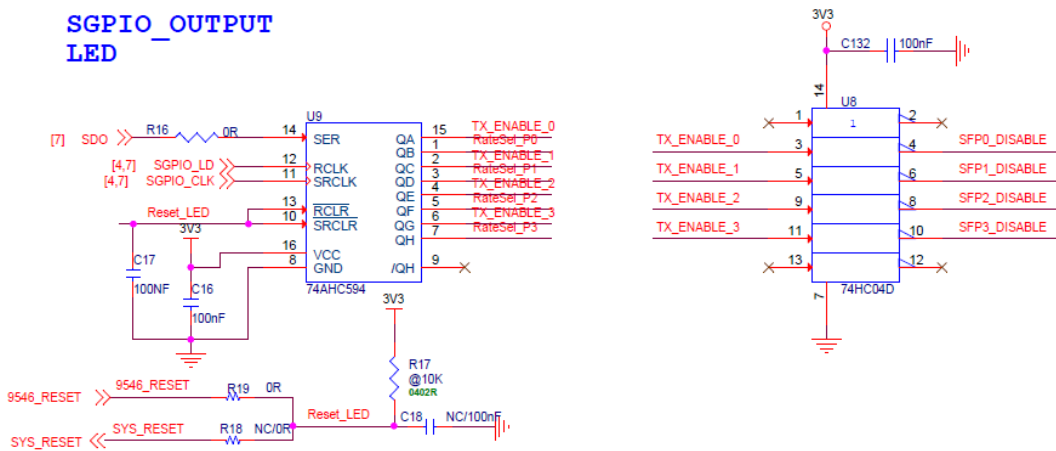
Reference Circuit of Gigabit SFP



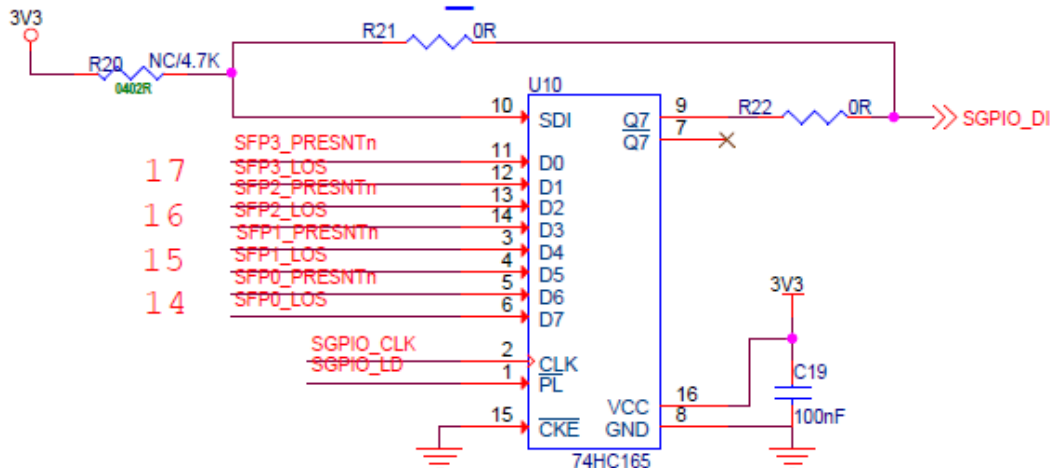
DDMI Reference Circuit



SGPIO_OUTPUT LED



SGPIO_INPUT

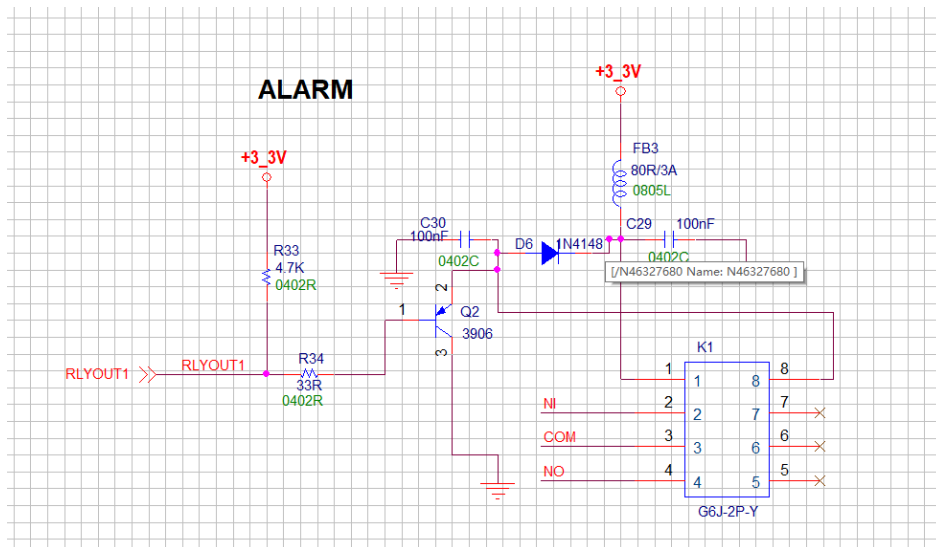




PIN name	Type	Configuration Method
UART_RX0	Input	Serial port 0 receiving Serial port 0 is only used for CONSOLE port
UART_TX0	Output	Serial port 0 transmitting Serial port 0 is only used for CONSOLE port

PIN name	Type	Function description
RLYOUT1	Output	Relay alarm output 1, active-high

Reference circuit of relay alarm



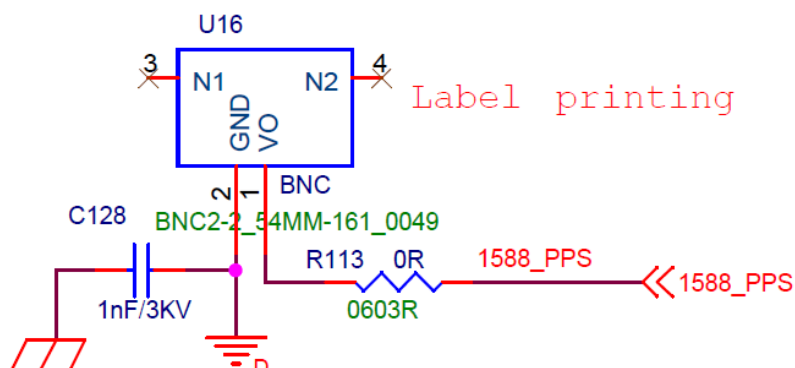
Note:

- In the power-off/on alarm state: COM and NI are conductive, and COM and NO are open.
- In the power-on no-alarm state: COM and NI are open, and COM and NO are conductive.

4.5 1588_PPS

PIN name	Type	Function description
1588_PPS	Bidirectional	1588_PPS signal data output

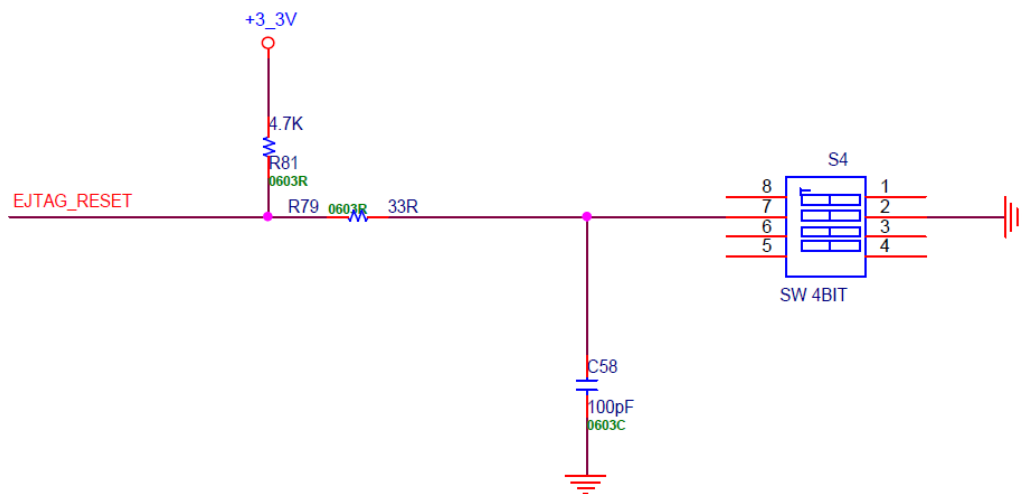
PPS Participation Circuit



4.6 Reset Interface

PIN name	Type	Configuration Method
EJTAG_RESET	Input	Input pin of manual reset, active low. When this pin is low level, the system will enter the reset and restart state.

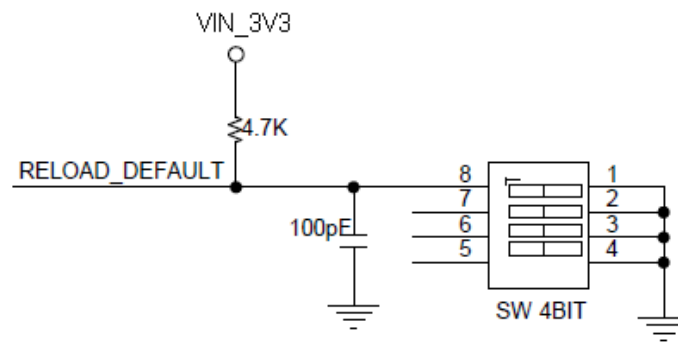
Reset Reference Circuit



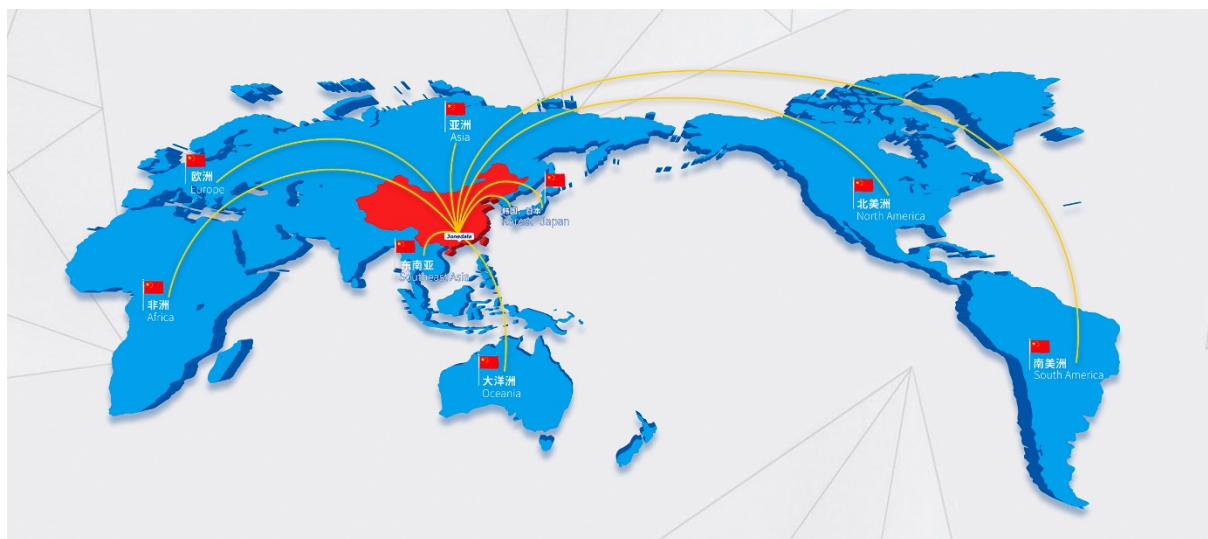
4.7 Restore Factory Settings

PIN name	Type	Configuration Method
RELOAD_DEFAULT	Input	Restore factory settings. Active low for 2s, it is recommended to pull high.

Reference Circuit of Restore Factory Settings



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